

61

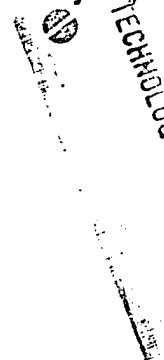
AF/ 2826

TSMC 98-338BC

Application no. 09/726,663



July 19, 2003



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TO: Commissioner of Patents  
P.O. Box 1450  
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FROM: Stephen Ackerman Reg. No. 37,761  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

SUBJECT: Serial # 09/726,663  
File Date: 11/30/2002  
Inventor: Lin, Chrong-Jung  
Examiner: Dickey, Thomas L.  
Art Unit: 2826  
Title: Step-Shaped Floating Poly-Si Gate To Improve Gate  
Coupling Ratio For Flash Memory Application

### RESPONSE TO OFFICE ACTION

This is in response to the Office Action dated 07/08/2003 Please amend  
the above identified application for patent as follows:

### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United  
States Postal Service as first class mail in an envelope addressed to :  
Commissioner of Patents, P.O. Box 1450, Alexandria VA 22313-1450  
On Sept. 8, 2003.

Signature George O. Saite Date: Sept. 8, 2003  
George O. Saite Reg. No. 19,572

TSMC 98-338BC

Application no. 09/726,663

Confirmation No. \_\_\_\_\_

Application No. : 09/726,663  
Applicant : Lin, Chrong-Jung  
File : 11/30/2002  
TC/A.U. : 2826  
Examiner : Dickey, Thomas L.

Docket No. : TSMC 98-338BC  
Customer No. : \_\_\_\_\_

Commissioner of Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

### AMENDMENT

Sir:

In response to the Office action of 07/08/2003, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.



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